

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,042,043 B2
APPLICATION NO. : 09/943134
DATED : May 9, 2006
INVENTOR(S) : Forbes et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (56), under "Other Publications", in column 2, line 5, delete "SiO2," and insert -- SiO₂--, therefor.

On the face page, in field (56), under "Other Publications", in column 2, line 5, after SiO₂," delete "and".

On the face page, in field (56), under "Other Publications", in Column 2, line 5, delete "Al₂O₃," and insert -- Al₂O₃--, therefor.

On the face page, in field (56), under "Other Publications", in column 2, line 5, delete "ZrO₃" and insert -- ZrO₃--, therefor.

On the face page, in field (56), under "Other Publications", in column 2, line 6, delete "ZrO₂" and insert -- ZrO₂--, therefor.

On the face page, in field (56), under "Other Publications", in column 2, line 8, delete "Al₂O₃" and insert -- Al₂O₃--, therefor.

In column 1, line 12, delete "Baffler" and insert -- Barrier --, therefor.

In column 1, line 21, delete "Baffler" and insert -- Barrier --, therefor.

In column 2, line 57, after "DEVICES"" delete "." and insert -- application serial no. 09/945514. --, therefor.

In column 21, lines 39-40, delete "Films," application Serial No. ____)." and insert -- Films,") --, therefor.

In column 25, line 11, in Claim 1, after "gate;" delete "and".

In column 25, line 15, in Claim 1, delete "insulator." and insert -- insulator; and the asymmetrical low tunnel barrier integrate insulator having lower barrier height for an erase operation than for a retention operation. -- , therefor.

In column 25, line 42, in Claim 8, after "with the" insert -- asymmetrical --.

In column 25, line 62, in Claim 10, delete "formed" and insert -- formed by an electric field --, therefor.

In column 26, line 11, in Claim 13, after "with the" insert -- asymmetrical --.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 26, line 14, in Claim 14, delete "vertical" and insert -- vertical, depletion mode --, therefor.

In column 26, line 14, in Claim 14, delete "claim 8," and insert -- claim 10, --, therefor.

In column 26, line 17, in Claim 15, delete "vertical" and insert -- vertical, depletion mode --, therefor.

In column 26, line 17, in Claim 15, delete "claim 12," and insert -- claim 14, --, therefor.

In column 26, line 20, in Claim 16, delete "vertical" and insert -- vertical, depletion mode --, therefor.

In column 26, line 20, in Claim 16, delete "claim 8," and insert -- claim 10, --, therefor.

In column 26, line 23, in Claim 17, delete "vertical" and insert -- vertical, depletion mode --, therefor.

In column 26, line 23, in Claim 17, delete "claim 14," and insert -- claim 16, --, therefor.

In column 26, line 63, in Claim 23, delete "claim 16," and insert -- claim 18, --, therefor.

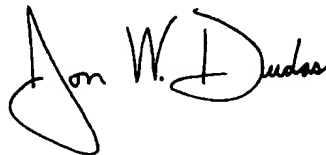
In column 26, line 64, in Claim 23, after "gate" delete "transistor".

In column 26, line 65, in Claim 23, delete "transistor".

In column 26, line 65, in Claim 23, delete "transistor".

Signed and Sealed this

Twenty-eighth Day of November, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office